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B.Tech. (Full Time) DEGREE END SEMESTER EXAMINATIONS, NOV / DEC 2012

ELECTRONICS AND COMMUNICATION ENGINEERING BRANCH

Fourth Semester

EC9302- Linear Integrated Circuits

(Regulations :2008)

)n: 3 Hrs. **Maximum Marks 100**

Answer All Questions

Part A 10x2=20 Marks

t is a current source?

ne slew rate.

: the schematic of op-amp based precision rectifier.

: the schematic of current to voltage converter.

t is compander IC?

t is a frequency synthesizer?

t is the role of EOC and SOC in Successive approximation type A/D converter?

t is an analog switch?

the different sources of noise.

at is an optocoupler?

Part B 5x16 =80 Marks

th a neat diagram explain the internal block of operational amplifier. (8)

xplain current mirror and derive the expression for the same. (8)

ith a neat diagram explain the working of log and antilog amplifiers. (16)

(OR)

With a neat diagram explain the working of inverting Schmitt trigger. (8)

Explain the working of op-amp based integrator and derive the expression for the same. (8)

P.T.O