

b. i. Find the resistance across A and B in the circuit given in figure 7 using delta to Y- (8) conversion.

ii. For the circuit given in figure 8, find the value of R_L for maximum power transfer. (8)

- 13. a. i. Explain the operation of zener diode voltage regulator.
 - ii. Draw and explain h parameter model of BJT.

(or)

(8)

(8)

(4)

(4)

- b. Explain the drain current flow in depletion mode and enhancement mode MOSFET for various values of Vgs
- 14. a. Derive the ripple factor, TUF, PIV and efficiency of bridge rectifier with neat circuit diagram and input/output waveforms.

(or)

- b. i. With equivalent circuit diagram, derive gain, input and output impedance of CE amplifier.
- 15. a. Explain the following op-amp applications.
 - i. Inverting and non-inverting amplifier (8)
 - ii. Subtractor
 - iii. Integrator

(or)

b. With neat diagram, explain the operation of digital to analog converter.